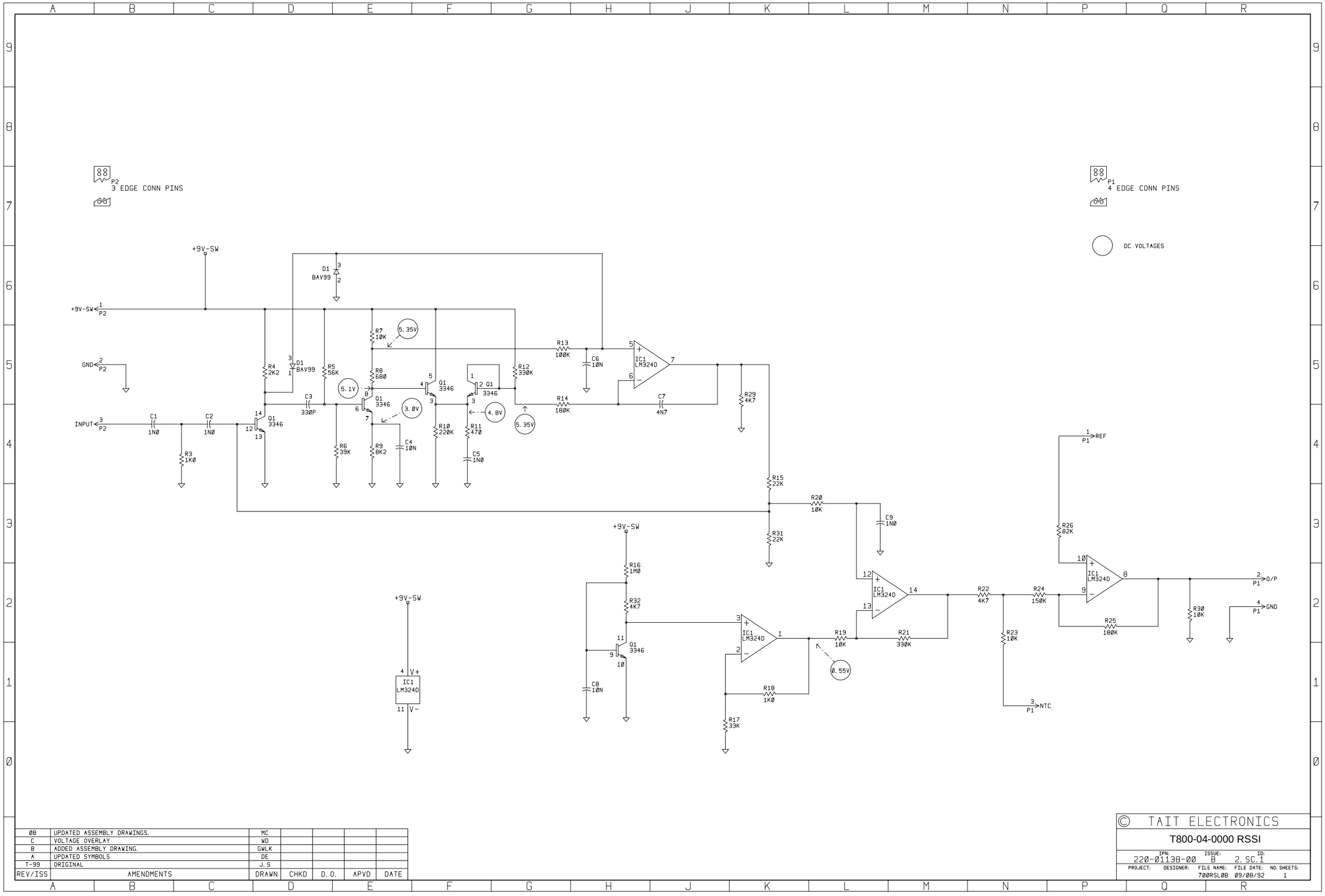

REV/ISS	AMENDMENTS	DRAWN	CHKD	D. O.	APVD	DATE
0B	UPDATED ASSEMBLY DRAWINGS.	MC				
C	VOLTAGE OVERLAY	WD				
B	ADDED ASSEMBLY DRAWING.	GWLK				
A	UPDATED SYMBOLS	DE				
T-99	ORIGINAL	J. S				

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PROJECT:	DESIGNER:	FILE NAME:	FILE DATE:	NO. SHEETS:
		700RSL0B	09/08/92	1

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